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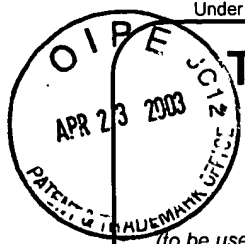
2827

PTO/SB/21 (08-00)

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Total Number Of Pages In This Submission	Application Number	10/077,211
	Filing Date	Feb. 14, 2002
	First Named Inventor	Richard Nathan
	Group Art Unit	2827
	Examiner Name	Zarneke, David A.
Attorney Docket No.		JIG006 US

ENCLOSURES (check all that apply)

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<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> Affidavits/declarations	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation of Previous Powers; And Statement Under 37 CFR 3.73(b)	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
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SIGNATURE OF APPLICANT, ATTORNEY OR AGENT

Firm or Individual Name	Omkar K. Suryadevara, Reg. 36,320 Silicon Valley Patent Group LLP 2350 Mission College Boulevard, Suite 360 Santa Clara, California 95054
Signature	<i>S. Omkar</i>
Date	April 23, 2003

Express Mail Label No.: _ ER 205 699 576 US _

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Richard Nathan; and Dale E. Means, Jr.;
Assignee: JigSaw Tek
Title: Integrated Circuit Package and Method For Fabrication
Serial No.: 10/077,211 Filing Date: Feb. 14, 2002
Examiner: Zarneke, David A. Group Art Unit: 2827
Docket No.: JIG006 US Confirmation No: 3098

Santa Clara, California
April 23, 2003

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicants submit for consideration in the above-identified patent application the documents listed on the accompanying Form PTO-1449. A copy of the documents is also submitted herewith. The Examiner is requested to make these documents of record.

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered by the Examiner, and made of record herein.

The information contained in this Information Disclosure Statement is to the best of my knowledge and is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, data, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

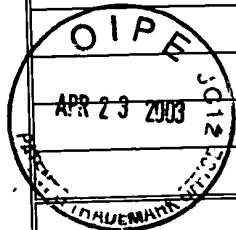
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ER 205 699 576 US**

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office	Application No.:	10/077,211
	Filing Date:	Feb. 14, 2002
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor:	Richard Nathan
(Use several sheets if necessary)	Group Art Unit:	2827
	Examiner Name:	Zarneke, David A.
	Confirmation No.:	3098
	Attorney Docket No.:	JIG006 US



U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	5,063,177	11/5/1991	Geller et al.	437	209	
	5,998,868	12/7/1999	Pogge et al.	257	730	
	5,963,429	10/5/1999	Philip Chen	361	764	
	5,783,856	7/12/1998	Smith et al.	257	618	
	6,075,711	6/13/2000	Brown, et al.	361	761	
	6,239,496	5/29/2001	Junichi Asada	257	777	
	4,835,598	5/30/1989	Higuchi et al.	357	72	
	6,316,278	11/13/2001	Jacobsen et al.	438	22	
	6,013,948	6/11/2000	Akram et al.	257	698	

Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass		Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner

Date Considered

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